
P6ED5-D4I-35/53T1

Features

- u Up to 6.25Gbps data rate
- u Up to 500m on 9/125 μ m SMF
- u Simplex LC receptacle optical interface
- u Single +3.3V power supply
- u Hot-pluggable
- u 1310nm FP Tx/1550nm PIN Rx for P6ED5-D4I-35T1
- u 1550nm FP Tx/1310nm PIN Rx for P6ED5-D4I-53T1
- u AC coupling of CML signals
- u DDM Function implemented
- u Internally Calibration
- u Operating temperature range:
Industry: -40°C ~85°C
- u Compliant with RoHS

Applications

- u Digital Wireless repeaters
- u Fiber channel
- u High speed data links
- u LTE

Standards

- u Compliant with MSA SFP+ specification
- u Compliant with SFF-8472(SFF-8431)
- u CPRI Line Rate Option: 6144Mbps

Specification

Absolute Maximum Ratings				
Parameter	Symbol	Min	Max	Unit
Storage temperature	T _s	-40	85	°C
Power Supply Voltage	V _{cc}	-0.5	+4	V
Relative Humidity	RH	5	95	%

Recommended Operating Conditions					
Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	T _c	-40		85	°C
Power Supply Voltage	V _{CC}	3.135	3.3	3.465	V
Data Rate		1	6.25		Gbps
Fiber Length 9m core SMF				500	m

Electrical Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Supply Current	ICC			300	mA	1
Transmitter Differential Input Voltage		200		1000	mV	
Receiver Differential Output Voltage		300	800	1000	mV	
Transmit Fault Alarm Voltage	V _{oh}	2.4		V _{cc} +0.3	V	LVTTL
	V _{ol}	-0.3		0.4	V	LVTTL
Loss of Signal(LOS)	V _{oh}	2.4		V _{cc} +0.3	V	LVTTL
	V _{ol}	-0.3		0.4	V	LVTTL
Transmit Disable Voltage	V _{ih}	2		V _{cc} +0.3	V	LVTTL
	V _{il}	-0.3		0.8	V	LVTTL

Optical transmitter Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Launched Power (avg.)	P _{OUT}	-6.5		0.5	dBm	
Operating Wavelength Range	λ _c	1260	1310	1360	nm	P6ED5-D4I-35T1
		1520	1550	1580		P6ED5-D4I-53T1
Spectral Width	Δλ			4	nm	FP

Extinction Ratio	ER	3.5			dB	2
RIN ₁₂₀ OMA	RIN			-128	dB/Hz	
Optical Output Power after TX Disable	P _{DIS}			-45	dBm	
Optical Receiver Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Wavelength Range	λ_C	1520		1580	nm	P6ED5-D4I-35T1
		1260		1360		P6ED5-D4I-53T1
Receiver Sensitivity	P _{SEN}			-12.5	dBm	4
Optical Power Input Overload	S _{AT}	0.5			dBm	4
Receiver Reflectance				-12	dB	
LOS	Optical Dessert	P _d		-16	dBm	
	Optical Assert	P _a	-30			
LOS hysteresis		0.5		5	dB	5

Note1. The supply current is SFP+ module's working current.

Note2: For the measurements, the device was driven with 2³¹-1 PRBS pattern

Note3. Optical transition time is the time interval required for the rising or falling edge of an optical pulse to transition between the 20% and 80% amplitudes relative to the logical 1 and 0 levels

Note4. Measured with a PRBS 2³¹-1 test pattern, @6.25Gbps, ER=4dB, BER<10-12

Note5. The LOS Hysteresis minimizes "chatter" on the output line. In principle, Hysteresis alone does not guarantee chatter-free operation

Digital Diagnostic Monitoring Information

Parameter	Accuracy	Calibration	Note
Temperature	±3°C	Internal	-40~85°C
Voltage	±3%	Internal	3.1 to 3.5V
Bias Current	±10%	Internal	Specified by normal value
TX Power	±3dB	Internal	-6.5~-0.5dBm
RX Power	±2dB	Internal	-16~-0.5dBm

Pin definition

The SFP+ modules are hot-pluggable. Hot pluggable refers to plugging in or unplugging a module while the host board is powered. The SFP+ host connector is a 0.8 mm pitch 20 position right angle improved connector specified by SFF-8431, or stacked connector with equivalent with equivalent electrical performance. Host PCB contact assignment is shown in Figure 1 and contact definitions are given in Table 5. SFP+ module contacts mates with the host in the order of ground, power, followed by signal as illustrated by Figure 2 and the contact sequence order listed in Table 5.

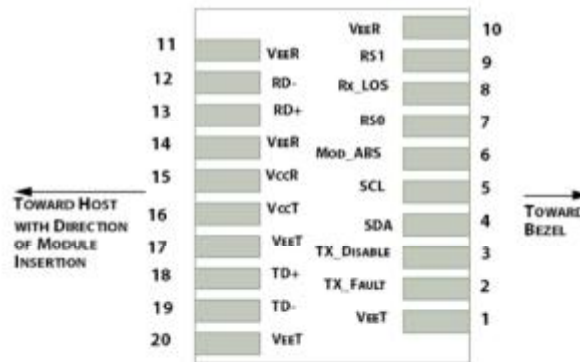


Figure 1: SFP+ Pad assignment Top View



Figure 2: SFP+ Module Contact Assignment

Pin	Symbol	Name/Description	Power Seq.	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground)	1st	1
2	TX_Fault	Transmitter Fault,Low:normal;High:abnormal	3rd	2
3	TX_Disable	Transmitter Disable. High: Transmitter off; Low: Transmitter on;	3rd	3
4	SDA	2-Wire Serial Interface Data Line(Same as MOD-DEF2 in INF-8074i).	3rd	4
5	SCL	2-Wire Serial Interface Data Line(Same as	3rd	4

		MOD-DEF2 in INF-8074i).		
6	Mod_ABS	Module Absent, Connect to VeeT or VeeR in Module.	3rd	5
7	RS0	Rate Select 0, optionally controls SFP+ module receiver.	3rd	6
8	RX_LOS	Receiver Loss of Signal indication. High: loss of signal; Low: signal detected;	3rd	7
9	RS1	Rate Select 1, optionally controls SFP+ module transmitter.	3rd	8
10	VeeR	Receiver Ground	1st	1
11	VeeR	Receiver Ground	1st	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O	3rd	9
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O	3rd	9
14	VeeR	Receiver Ground	1st	1
15	VccR	Receiver Power Supply	2nd	10
16	VccT	Transmitter Power Supply	2nd	10
17	VeeT	Transmitter Ground	1st	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I	3rd	11
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-I	3rd	11
20	VeeT	Transmitter Ground	1st	1

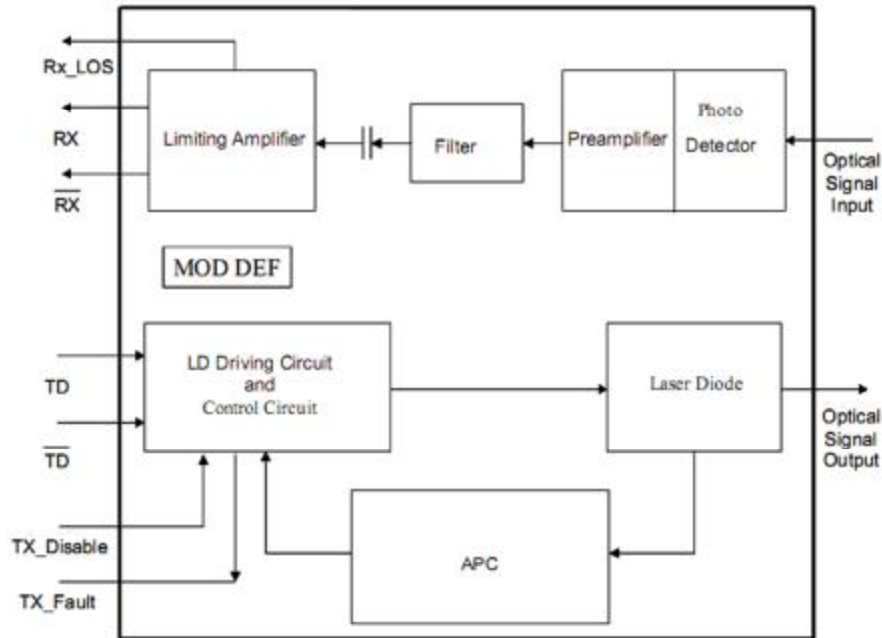
Table 5: SFP+ Module PIN Definition

Power Seq.: Pin engagement sequence during hot plugging.

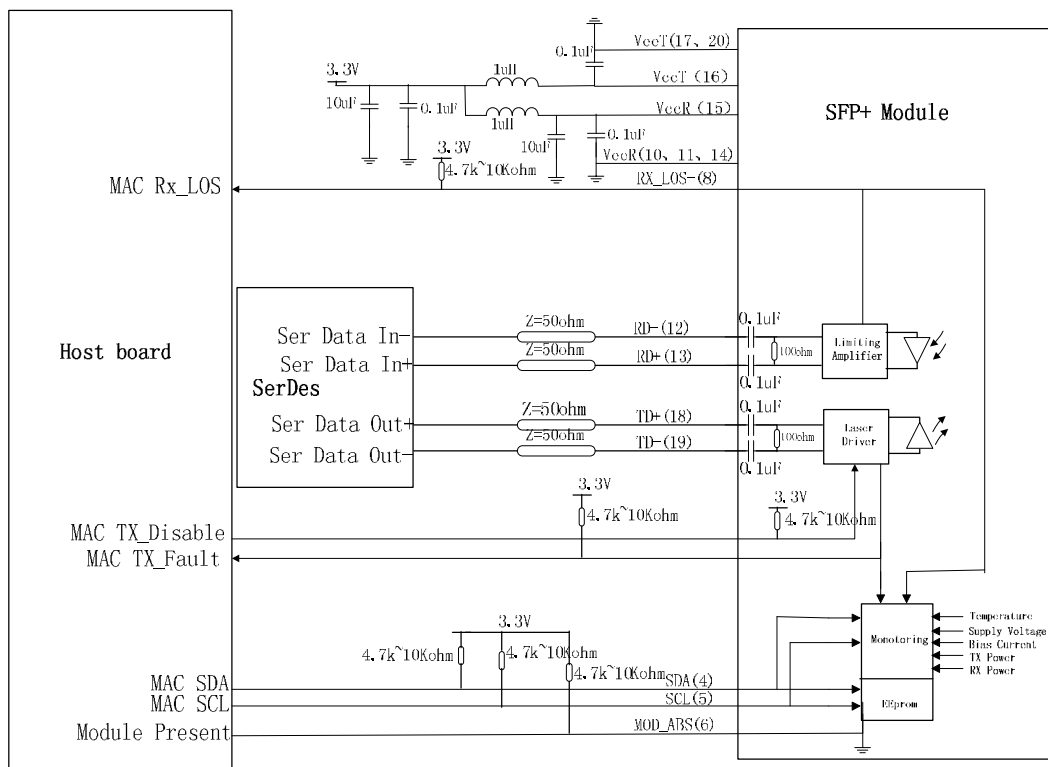
Notes:

1. The module signal ground contacts.
2. This pin is an open drain/collector and should be pulled up to Vcc-host in the host with a 4.7k~10k Ohm resistor.
3. This pin should be pulled up to Vcct with a 4.7k~10k Ohm resistor in modules.
4. SDA&SCL(IIC) are need pull up 4.7k~10k Ohm resistor on host board.
5. Mod_ABS is connected to VeeT or VeeR in the SFP+ module.
6. Rate Select 0, Optionally controls SFP+ module receiver , High: RX input signaling rate > 4.25GBd and Low: RX input signaling rate ≤ 4.25GBd.
7. Module RX_Los of signal indication , need pull up 4.7k~10k Ohm resistor on host board.
8. Rate Select 1, Optionally controls SFP+ module transmitter, High: Tx input signaling rate > 4.25GBd and Low : Tx input signaling rate ≤ 4.25GBd.
9. RD -/+ : These are the differential receiver outputs. They are CML AC-coupled with 100 Ohm terminal resistor matching internal.
10. VccR and VccT are the receiver and transmitter power supplies.
11. TD -/+ : These are the differential transmitter inputs. They are CML AC-coupled with 100 Ohm terminal resistor matching internal.

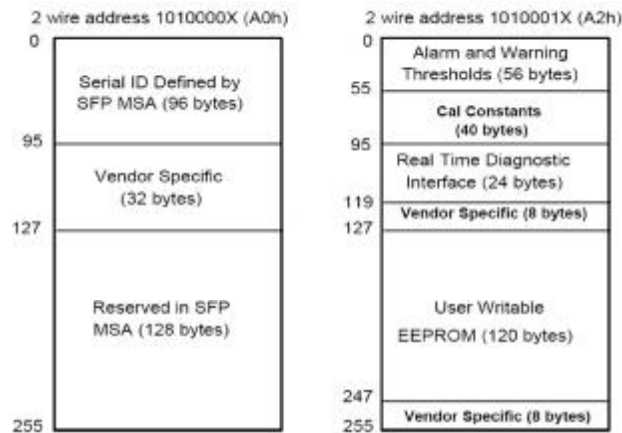
Block Diagram



Typical application Circuit



EEPROM Memory Map



EEPROM Serial ID Memory Contents

The optical transceiver contains an EEPROM. It provides access to sophisticated identification information that describes the transceiver’s capabilities, standard interfaces, manufacturer, and other information. When the serial protocol is activated, the host generates the serial clock signal (SCL, Mod Def 1). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP+ transceiver. The negative edge clocks data from the SFP+ transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data fields define as following.

EEPROM Serial ID Memory Contents (2-Wire Address A0h)

Address	Name of field	Hex	Description
BASE ID Fields			
00	Identifier	03	SFP+ transceiver
01	Ext.Identifier	04	Serial ID module supported for SFP+
02	Connector	07	LC
03-05	Transceiver Codes	00 00 00	Not defined
06	Transceiver Codes	00	Not defined

07-10	Transceiver Codes	00 00 00	Not defined
11	Encoding	03	Encoding codes
12	BR,Nominal	3E	Nominal 6GHz transmitter
13	Rate Identifier	00	Not defined
14	Length(9um)-km	00	0.5Km@9/125um SM fiber
15	Length(9um)-m	5	500m@9/125um SM fiber
16	Length(50um)	00	Not support MMF
17	Length(62.5um)	00	
18	Length(cable)	00	Not support cable
19	Length(OM3)	00	Not support OM3
20-35	Vendor Name	4D 45 4E 54 45 43 48 4F 50 54 4F...	"MENTECHOPTO"(ASCII character)
36	Reserved	00	Not defined
37-39	Vendor OUI	00 00 00	Not defined
40-55	Vendor P/N		"
56-59	Vendor P/N Rev.	31 2E 30 20	"1.0" (ASCII character)
60-61	Laser Wavelength	05 1E/06 0E	1310nm/1550/nm
62	Reserved	00	Not defined
63	CC_BASE	XX	Check sum of bytes 0-62
Extended ID Fields			
64-65	Options	00 1A	RX_LOS、TX_Fault are implemented
66	BR, max	14	Upper bit rate margin,20%
67	BR, min	14	Lower bit rate margin,20%
68-83	Vendor SN	MA.....xx	Vendor Serial Number in ASCII character
84-91	Date Code	Data Code	Vendor Date Code in ASCII character
92	Diagnostic Monitoring Type	68	Digital Diagnostic monitoring implemented "External calibrated" is implemented, RX measurement type is "Average Power"
93	Enhanced options	F0	Optional Alarm/warning flags, soft Tx_Fault monitoring, soft LOS monitoring are implemented
94	SFF-8472 compliant	05	SFF-8472 compliant with revision 11.0
95	CC-EXT	XX	Check sum of bytes 64-94
Vendor Specific ID Field			
96-127	Vendor Specific	00	Vendor specific EEPROM
128-255	Reserved	00	Reserved for future use

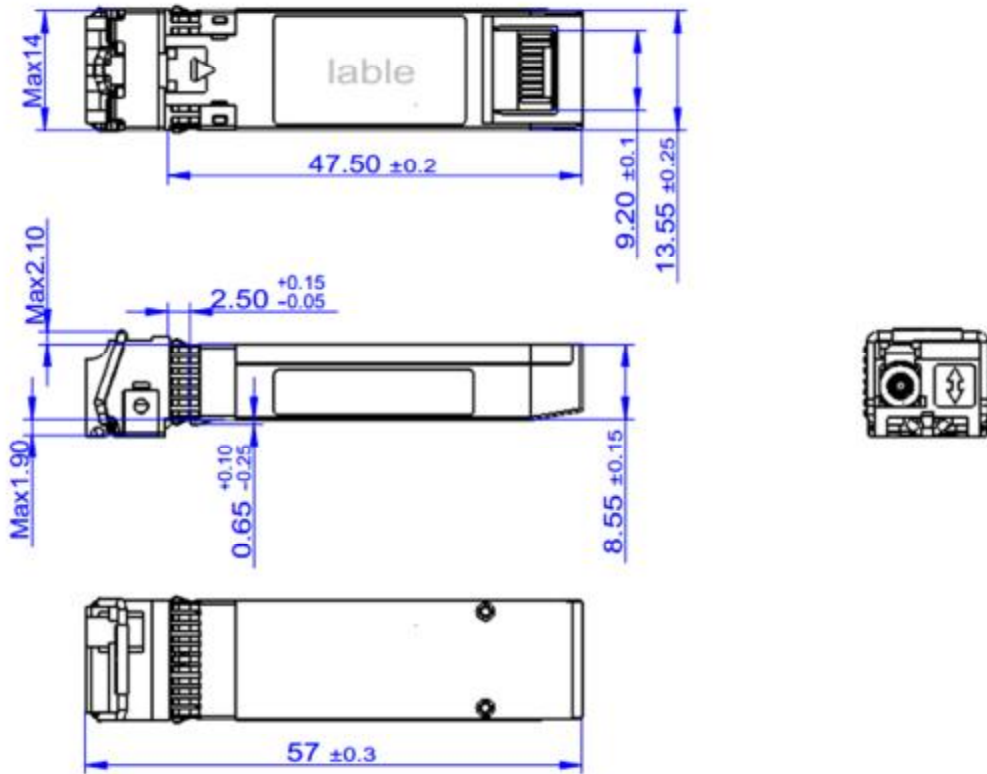
Digital Diagnostic Monitoring Interface: Alarm and Warning Thresholds

(2-Wire Address A2h)

Address	#Bytes	Name	Real Value	Unit	Hex
00-01	2	Temp High Alarm	100	°C	
02-03	2	Temp Low Alarm	-50	°C	
04-05	2	Temp High Warning	85	°C	
06-07	2	Temp Low Warning	-40	°C	
08-09	2	Voltage High Alarm	3.7	V	
10-11	2	Voltage Low Alarm	3	V	
12-13	2	Voltage High Warning	3.5	V	
14-15	2	Voltage Low Warning	3.1	V	
16-17	2	Bias High Alarm	90	mA	
18-19	2	Bias Low Alarm	4	mA	
20-21	2	Bias High Warning	80	mA	
22-23	2	Bias Low Warning	5	mA	
24-25	2	TX Power High Alarm	2.5	dBm	
26-27	2	TX Power Low Alarm	-8.5	dBm	
28-29	2	TX Power High Warning	0.5	dBm	
30-31	2	TX Power Low Warning	-6.5	dBm	
32-33	2	RX Power High Alarm	2.5	dBm	
34-35	2	RX Power Low Alarm	-14.5	dBm	
36-37	2	RX Power High Warning	0.5	dBm	
38-39	2	RX Power Low Warning	-12.5	dBm	
40-55	16	Reserved	Reserved		

Package Outline

Dimensions are in millimeters. (unit: mm)



Regulatory Compliance

Feature	Test	Method
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1 (>1.5kV) – Human Body Model
Electrostatic Discharge (ESD) Immunity	IEC61000-4-2	Class 2(>4.0kV)
Electromagnetic Interference (EMI)	CISPR22 ITE Class B FCC Class B CENELEC EN55022 VCCI Class 1	Comply with standard
Immunity	IEC61000-4-3	Comply with standard
Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1,2	Compatible with Class I laser Product

Ordering information

Part. No	Specifications								
	Pack	Rate (Gbps)	Tx (nm)	Po (dBm)	RX	Sen (dBm)	Temp (°C)	Reach (m)	DDM
P6ED5-D4I-35T1	SFP+	6.25	1310	-6.5~0.5	1550	<-12.5	-40~85	500	Y
P6ED5-D4I-53T1	SFP+	6.25	1550	-6.5~0.5	1310	<-12.5	-40~85	500	Y